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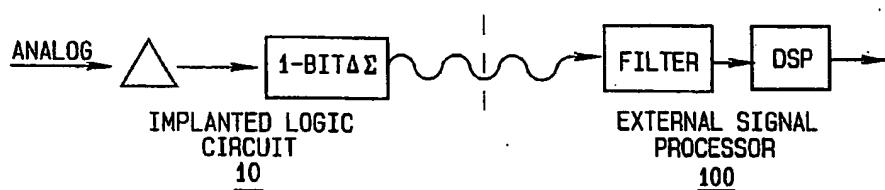
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(54) Title: METHOD AND APPARATUS FOR WIRELESS BRAIN INTERFACE



(57) Abstract: An implantable logic circuit configured to receive analog bioelectric signals from one or more implanted electrodes, perform amplification, A/D conversion of the received analog bioelectric signals, signal sampling, and to communicate the signals to a remote processing system over a wireless communications link. Power for the implantable logic circuit is derived from an external source over a wireless link.

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